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Architecture

8085 Microprocessor

The Intel 8085 is an

8-bit microprocessor

introduced by Intel in

1977. It is an 8-bit

general purpose

microprocessor

capable of addressing

64K of memory. It is

Single NMOS device.

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It contains 6200 transistors approx. The device has 40 pins Dual Inline-Package(DIP), requires a +5V single power supply, and can operate with a 3-Mhz clock.

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Architecture and
Memory Interfacing by
Rahul Patel, Assistant
Professor, EC Dept.,
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College of
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– During the execution of the instruction, these lines carry the low-order address bits during the early part of the cycle, then during the later part of the cycle,

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they carry the 8 data bits. – This is known as multiplexing the bus.

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28 Register Array •

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The programmable registers were discussed in the last chapter. Two additional registers W and Z are included in the register array.

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The Control and
Status Signals • IO/M:
This is a status signal
used to differentiate
between I/O and

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memory operations. –

When it is high
(IO/M=1), it indicates
an I/O operation. –

When it is low
(IO/M=0), it indicates
a memory operation.

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5.1.2 Address Buffer
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to Microprocessor

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- The microprocessor reads one instruction at a time, matches it with its instruction set, and performs the data manipulation specified.
 - The result is either stored back into memory or displayed on an output device.
- ### 3. THE 8085

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The 8085 uses three separate busses to perform its operations

- The address bus.
 - The data bus.
 - The control bus.
- 4.

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After the request has
been processed, the
control shifts back to
the main program.

This interrupt might
be a hardware or a
software interrupt.

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Some interrupts can be ignored (or maskable) while some cannot (non-maskable). There are five interrupt signals in 8085

microprocessor:

INTR, RST 7.5, RST 6.5, RST 5.5, TRAP.

Increment or

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8085 and Its Busses.

The 8085 is an 8-bit
general purpose
microprocessor that
can address 64K Byte
of memory. It has 40
pins and requires 5V
power supply. It can
run at a frequency of
3 MHz(or 5 MHz). The
pins on the chip can
be grouped ...

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MCQ Questions This
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